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## Abstract

We introduce a novel optimization engine for META4, a new object oriented language currently under development. It uses Static Single Assignment (henceforth SSA) form coupled with certain reasonable, albeit very uncommon language features not usually found in existing systems. This reduces the code footprint and increased the optimizer's "reuse" factor. This engine performs the following optimizations; *Dead Code Elimination* (DCE), *Common Subexpression Elimination* (CSE) and *Constant Propagation* (CP) at *both runtime and compile time* with linear complexity time requirement. CP is essentially free, whether the values are really source-code constants or specific values generated at runtime. CP runs along side with the other optimization passes, thus allowing the efficient runtime *specialization* of the code during any point of the program's lifetime.

## 1. Introduction

A recurring theme in this work is that powerful expensive analysis and optimization facilities are not *necessary* for generating *good code*. Rather, by using information ignored by previous work, we have built a facility that produces good code with simple linear time algorithms. This report will focus on the *optimization* parts of the system. More detailed reports on META4?; ? as well as the compiler are under development.

Section 0.2 will introduce the scope of the optimization algorithms presented in this work. Section 1. will discuss some of the important definitions and concepts related to the META4 programming language and the optimizer used by the algorithms presented herein. The special features of META4 allow for linear (or near-linear) complexity optimization, along with an essentially free run-time constant propagation that is implemented by a simple, compact engine. The code size for the entire optimization engine (without the header and support routines) is less than 250 lines of semi-dense C++ code.

Section 2. will discuss in more detail the background information on the optimization algorithms, which are presented in section 3. (and discussed with more detail in ?). Sections 4. and 5. end with discussion of related work, lessons learned and future directions.

### 1.1 Optimization

Optimizations that do not *directly* modify control flow, such as Dead Code Elimination (DCE), Constant Propagation (CP), Common Subexpression Elimination (CSE), and even *Runtime Constant Propagation* (RCP) can be implemented in a two pass algorithm — each optimization<sup>1</sup> has a prepass algorithm that generates the needed data structures. They share a single *postpass algorithm* which actually carries out all actions.

Let  $N$  be the size of the program<sup>2</sup> and let  $h(p)$  be the cost of the hash function for keys of some constant length  $p$ . Then, as we will see in 3.6, the prepas is at most  $O(h(p)N)$  and the postpas is  $O(N)$ . If  $h(p)$  is a constant, this reduces the complexity to  $O(N)$ , i.e. linear time.<sup>3</sup>

META4 implements Partial Evaluation (henceforth PE)?; ?; ? as CP at execution time. The programmer is able to generate and invoke specialized versions of *any* code segment with specific run time values treated as constants and propagated through (and re-optimized) at *any time* during the lifetime of the program. META4 supports (R)CP as well as unrestricted Run Time Code Generation.

This work will also show that CP, even at runtime does not explicitly require a separate prepass but can run as a part of the other optimizations featured here. The programmer only needs to specify that a particular *instance* is a *known* value and instruct the *optimizer* to generate a specialized form of the routine(s) that use this value. To the best of the author's knowledge, this is the first work that *combines* these optimizations along with runtime CP that is executed by a *single, unified engine* in linear (or near linear) time.

## 2. META4: Language Design Compiler Basics

Before the actual optimization engine can be discussed in detail, it is first necessary to briefly mention the some of the important attributes of the META4? language which *actively* contribute to the optimization process.

The first definition addresses the issue of *storage*. A unit of storage in META4 is known as an *instance*; and is identified by a pair of numbers. The first number identifies a unique instance which correspond to a textual variable name in the source code, and the second specifies the Static Single Assignment? index for that instance. The SSA index guarantees that an *instance* has one unique point of creation. Storage or data members of classes do exist in META4, but they are not directly accessible. Rather we introduce a single concept, *property* that generalizes and expands on data members and member functions.

A *property* is a behavioral trait of a class that is typed, named, inheritable, specializable, generalizable. It may require arguments, and may also return values as well. A crude analog would be a virtual member function of a class in C++ or Java.<sup>4</sup> Note that within META4, the *only way* an instance can be accessed from a class is through a *property*?. Furthermore the *property* is the only mechanism for communicating with an object in META4. Operations that are generally considered as primitives (such as add, subtract etc..) are also properties (of the integer class). The unification of storage and function invocation allows META4 language to separate storage from inheritance. Its definition also specifies the types and data-flow directions of all parameters. In addition a property may be declared as a *predictable* property which will be described later.

An *invocation* is an abstract wrapper for the binding of a *property* with its actual arguments. There are two main types of *invocation*, a concrete *statement* which binds arguments to the property being invoked and a *metastatement* which is a subtype of *invocation* used for encapsulating control flow change. A nonempty list of *properties* serves as the “continuation.” For example, a “loop” *metastatement* contains two *properties*, a “test” continuation and a “body” continuation. The latter is conditionally (re)executed pending the result from the “test”. The two subclasses of *invocation*, *metastatement* and *statement*, are combined to form structured control-flow? equivalent of the program.

All code segments, a.k.a. *implementations*, are sequences of *invocations*. An *implementation* defines the body of a *property*. It is an array of *invocations* that

also contains information on the instances used within that implementation. If a property can accept as arguments multiple *types* for the same parameter, then it has more than one implementation. As long as the actual type for that parameter is known at compile time<sup>5</sup>, a new implementation can be generated that better meets the argument specifications. This can potentially aid in producing better (*i.e.*, faster executing) code by avoiding dynamic dispatch for invocations as well as through RCP. This also directly supports *polyvariant specialization* and *polyvariant division*? as a property can have an implementation that are generated for specific argument *values* as well as specific *types*.

## 2.1 Synergistic Cooperation Between Programmer, Compiler and the Language

In META4, certain keywords are provided, so that the programmer can use them to inform the compiler of information that would be very useful and difficult to obtain automatically. These are analogous to certain annotations used in various PE engines for the language C?; ?; ?. Similar ideas are used here to help the optimizer in improving the code. However, in this work, “annotations” are not added onto an existing language, but are an integral part of the programming process that has been designed from ground up to support principled use of Run Time Code Generation (RTCG). This entices the programmer to work with the language and the compiler in producing good code.

The programmer specifies the dataflow to and from a property. Thus the compiler knows exactly which parameters import data to the property (*i.e.* are *incoming* parameters), which parameters return data (*i.e.* are *outgoing* parameters), and which parameters do both (*i.e.* are *in/out* parameters). Also, many “primitive” functions, such as integer operations have predictable results (*i.e.*, given the same input, they always produce the same output, and have no side-effects.). This predictability can be exploited in numerous optimizations. Many user defined properties are also predictable in the same manner as “primitives,” but automatically detecting this is generally difficult. However, in META4, due to the explicit data flow, SSA and careful definition of primitives, guaranteeing the correctness of predictability is easy. It is impossible for a programmer to mark a nonpredictable property as predictable. Regardless, when the programmer correctly marks a property as predictable, both the programmer and the compiler benefit from this explicit presence of semantic information. Therefore, we encourage the programmer to specify these as *predictable properties*.

These keywords, along with the nature of *properties* and *predictable properties* serve to shortcut some of the analysis that a compiler would need to carry out. They play a critical role in optimization as well as during RTCG and PE.

## 3. Optimization in META4

Previously reported optimization algorithms ?; ?; ? operate on intermediate forms of “primitive” operations, and sometimes group optimization algorithms together to form more expensive<sup>6</sup> and more powerful combined optimization ?; ? that allows them to find and optimize complex structures not amenable to repeated passes of separate optimizations.

The linear complexity algorithms presented here are arguably weaker than the more expensive combined approaches in ?; ? but make up for it by allowing for the direct manipulation of user defined *properties*, not just “primitives.” For purposes of the

optimization algorithms presented here, there are no differences between “primitive” *properties* and user defined *properties*. The only difference is that “primitives” have direct machine code form, while user defined properties are usually composed of these “primitives”.

These aid compilation in three ways. First, common subexpression elimination can now eliminate common calls to complex user defined properties. Second, the compiler knows exactly which parameters to a property will change, and which will not, that is data-flow is made explicit to both programmer and compiler, and is strongly enforced. Third, SSA adds additional data-flow information to the compiler, i.e. a tighter knowledge of what changes where.

Rather than using a powerful and expensive analysis engine which tries to guess which part is known, specializable, or not known, the knowledge of the programmer is exploited, and by supplying enough tools and fast, simple yet effective analysis routines, the programmer can specialize (manually or otherwise) during any phase of its life cycle (even at runtime of the program itself) any algorithm that can be specialized. In addition, the compiler itself is available to the programmer as a class and can be specialized, increasing the efficiency for appropriate tasks.

During the optimization pass (whenever that may be), invocations of *predictable* properties that have all incoming parameters KNOWN can be marked DEAD and once the invocation is activated, all outgoing parameters can be marked KNOWN. This operation can occur at any time during the lifetime of the program. Since values generated at runtime can be marked as *known* and be propagated, this is a valid form of runtime specialization of the program.

In reiteration, the specialization options available are runtime constant propagation and programmer controlled inlining/unrolling of statements/loops<sup>7</sup>.

## 4. Algorithms and Implementation

The overall design of the optimization engine in the META4 compiler is extremely simple yet powerful in its capabilities. Each application of the optimization engine is composed of one or two passes. DCE and CSE require a prepas to generate the information required in the second pass.

There is a main driver routine called *traverse(impl, op)* which traverses through the intermediate representation *impl*.<sup>8</sup> The argument *op* is a routine which processes an invocation. *traverse()* invokes *op* on each invocation in *impl*. The second pass is merely the application of *traverse()* to the implementation *impl* with a special *op* called *postpass\_apply()*. The following sections 3.2, and 3.3 explain the prepas algorithms. The postpass algorithm is explained in section 3.5.

### 4.1 Helper Routines

In the SSA world, an instance which is created by assignment has a predecessor, which is the right hand argument. e.g. assuming  $x_i = y_j$ , then the *predecessor*( $x_i$ ) is  $y_j$ . SSA enforces that  $x_i$  contains the exact same value as  $y_j$ .

The routine *isdead(i, impl)* returns TRUE if the argument *i* is DEAD within *impl*. A variable *v* is DEAD if it already has a predecessor and its only use is to construct other variables. If a variable is only used as a copy for another, and it has a predecessor, then the variable need not be created, and the predecessor can be used in its place.

## 4.2 Dead Code Elimination (DCE)

In this SSA world, *dead code* is any invocation which modifies non-external variables. These variables also must not be used as incoming parameters to other invocations. *Dead conditional branch elimination* is handled as part of the postpass. As *traverse()* processes each invocation, the special postpass operator *postpass-apply()* handles any cleanup duties required for metastatements, such as killing off any dead branches and aligning the SSA indices of variables. The actual DCE preprocess algorithm is called *prepdc()* and is used as an operator argument to *traverse()*. *prepdc()* actually marks statements LIVE, if they use or modify an argument (e.g. external parameter) to the property that *impl* implements. All statements not marked LIVE will be considered for removal during the postpass.

## 4.3 Common Subexpression Elimination (CSE)

Assume some predictable property *fp* and within some implementation *impl*, the set of chronologically ordered list of invocations of *fp* in *impl* called *S*. There exists a subset of *S* called *S<sub>equiv</sub>* with one or more invocations *s<sub>k</sub>* such that for all incoming parameters *p<sub>i</sub>* in *fp* and for all *s<sub>a</sub>, s<sub>b</sub>* in *S<sub>equiv</sub>*, *nth-arg-of(s<sub>a</sub>, i) equals nth-arg-of(s<sub>b</sub>, i)*. Trivially, each invocation in *S* can be its own *S<sub>equiv</sub>*, but the goal here is to partition *S* into its largest equivalent subsets, given the current state of information available to the compiler. All invocations in *S<sub>equiv</sub>* are considered to be *equivalent*. Thus all but the chronologically first invocation *s<sub>1</sub>*, in *S<sub>equiv</sub>* can be marked DEAD and the future uses of the outgoing parameters for the rest of the statements in *S<sub>equiv</sub>* can be replaced by the respective outgoing arguments of *s<sub>1</sub>*.

When *traverse(generate\_S)* below is applied to an implementation *impl*, it generates the set *S* for each predictable property invoked in *impl*.

```
generate_S(statement s, impl)
prop = invoked_property(s)
if prop is a predictable, then add s to the end of S for prop.
```

*Prepcse()*, the second preprocess for CSE partitions each *S* generated above into a list of *S<sub>equiv</sub>*.

```
prepcse(implementation impl):
for_each entry S in impl.S_list
    if (partition(S, list_of_S_equiv))
        for_each entry f in list_of_S_equiv DO
            statement base is first invocation in f
            for_each remaining statement sj in f { mark sj DEAD
                for_each outgoing parameter argi of sj
                    predecessor(argi) = args(base, i) }
```

In *prepcse()*, *list\_of\_S\_equiv* is a hash table containing a list of invocations that partitions a particular *S*. The hash key used to access *list\_of\_S\_equiv* is an array of instances<sup>9</sup> comprised of the incoming parameters of the invocation.

The following routine actually partitions *S* into non intersecting sets of invocations *S<sub>equiv</sub>*, and returns TRUE if at least one *S<sub>equiv</sub>* has more than one invocation in it. Of course, *h()* is the hash function for an instance list.

```
partition(invocation_idx_list S, list_of_S_equiv)
```

```

integer c = 0; for_each invocation  $pi$  in  $S$  {
     $hkey$  = concatenation of all incoming params of  $pi$ .
    if  $hkey$  is already in  $list\_of\_S\_equiv$  then  $c = c + 1$ ;
    insert  $pi$  into the end of  $list\_of\_S\_equiv[h(hkey)]$  }
return  $S.size() > c$ ;

```

#### 4.4 Breaking the Single Assignment Chain (BSAC)

*Prep\_bsac()* finds all chains of non-useful (*i.e.*, non-overlapping) generation of variables and marks them DEAD and sets the predecessor for the killed variables. (Runtime) Constant propagation will work, even after the SSA chain has been broken, as any SSA chains that had conflicting usage frontiers are NOT broken. The only other requirement is that the meta-level information for all related entities be available when creating a new implementation of a property.

```

prep_bsac(statement  $s$ ,  $impl$ )
if  $s$  is not DEAD or LIVE; then  $prop = invoked\_property(s)$ 
if is_assignment( $prop$ ) and last use of right hand arg of  $s$  is  $s$  then
    mark  $s$  DEAD; mark the left hand argument of  $s$  DEAD;
    set the left hand argument's predecessor as the right
        hand side of  $s$ 

```

#### 4.5 The Post Pass Algorithm and the driver

The *postpass\_apply()* routine uses the information generated in the prep pass and actually carries out the optimization/specialization as needed. The *activate()* call within *postpass\_apply()* invokes the property bound to the current statement and updates the StorageTag<sup>10</sup> records of all outgoing parameters. These new *known values* can then be used as arguments to other properties.

```

postpass_apply(statement  $s$ ,  $impl$ )
if  $s$  is not DEAD or LIVE then  $prop = invoked\_property(s)$ ;
for_each argument instance  $arg$ 
    if isdead( $arg$ ) then replace  $arg$  with predecessor( $arg$ )
    if all incoming args are KNOWN and is_predictable( $prop$ )
        activate( $s$ )* and mark  $s$  DEAD; mark all outgoing params KNOWN;

```

This next routine is the main driving engine: The argument  $op$  is any of the routines above that have the correct interface. The job of the *op.pre\_process()* and *op.post\_process()* is to handle any setup and cleanup required for processing metastatements. The only operator that has a defined pre\_process() and post\_process() is *postpass\_apply()*. Specifically, it kills any dead branches. Deciding to kill a branch is as simple as considering the number of live invocations in the implementation of that branch. If it is zero, then the branch can be killed. At runtime, for loops and conditionals, if the test variable is KNOWN, than the appropriate branch can be killed.

```

traverse( $impl$ ,  $op$ ) : for each invocation  $s$  in  $impl$ 
    if is_metastatement( $s$ ) then  $op.pre\_process(s)$ ;
        for_each continuation  $c$  in  $s$  traverse( $c.impl$ ,  $op$ )
             $op.post\_process(s)$ 
        else  $op(s, impl)$ 

```

## 4.6 Costs of the Passes

Each operator argument to *traverse()* is executed once per invocation. Assuming  $C$  is the cost of the operator and  $N$  the size of the program, the cost of the optimization is  $O(CN)$ .  $C$  is either 1 in the case of *generate\_S()*, or is proportional to the number of *parameters*  $p$  to a property, which can be considered a constant. The routine *prepcse()* is executed once per pass, not once per invocation like the other operators. *prepcse()* processes each candidate invocation exactly once, and the total number of candidate invocations is trivially less than  $N$ . Therefore its complexity is  $O(h(p)N)$  where  $h(p)$  is the hash predictable for the constant length  $p$ . It is arguable that for an optimistic view of the hashing cost, this reduces the overall complexity to  $O(N)$ .<sup>11</sup>

## 5. Related Work

In ?, Click and Cooper report that that an optimization framework simultaneously combining constant propagation, global value numbering (a form of CSE) and dead code elimination is possible. The cost is quadratic in the size of the program, but this cost is mitigated by the fact that the combined framework can detect and optimize away some program structures undetected in separate, iterated applications of its parts. There is an explicit tradeoff with the optimization engine presented here. While being linear in complexity, the CSE engine presented in section 3.3 is arguably weaker than the *global value numbering* based expression matchers presented in ? and reworked in ?; ? since it can not directly detect equivalent compound structures of “primitives.” (e.g., two loops that calculate the same value). However, this lack is mitigated by the fact that additional programmer-controlled information is available as *language features* to the compiler. Specifically, it is possible to group certain invocation sets as a user defined “predictable property” which will allow equivalent invocations of the property to be weeded out by the CSE engine; thus encouraging programmers to work with the compiler in generating good code.

Along with a sacrifice in power of the CSE engine, another feature here is that constant propagation is essentially free. That is, there needn’t be a separate pass of the engine for propagating them. All that needs to be done is a certain meta-level instance (that correspond to a real instance) be marked KNOWN at some point during the lifetime of the program, and the next time the postpass is run, the constant propagation proceeds side by side, similar in spirit to what the more complex combined optimization ?; ?; ?; ? frameworks do. The only requirement here is that along with the actual machine code of the routine being specialized, the meta-level information (specifically the implementation for the property and all other related structures) must be available to the compiler at specialization time. Of course, the *activate()* call would not be made unless ALL parameters are KNOWN at that time.

The need for guessing when to carry out inter-procedural analysis ?; ? is neatly avoided here as well. The only time inter-procedural analysis is performed is at the beck and call of the programmer, that is when the programmer hints that an invocation is *inlined* at its call site or when there are *metastatements* within an implementation (*metastatements* are always “inlined”). This is in keeping with the philosophy espoused here, which is that the programmer can provide necessary hints so that the compiler can do its job efficiently and productively, and these hints are provided as necessary language features explicit in the program, not from an outside analysis

facility. However, this does not preclude the future addition of an analysis engine should the need arise.

## 6. Conclusion

As software increases in size and complexity, it is vitally important that it be organized, designed and produced in a reusable, updatable manner. This inevitably leads to abstractions that negatively impact performance. Yet by allowing the programmer to *remove* such abstraction layers by *specialization* when needed, it is possible to recapture some of the inherent performance lost to the abstraction layers.

The SELF programming language ? and compiler feature automated runtime profiling, which is currently missing from the algorithms presented here and would likely be a useful addition. Also, the current lack of certain control-flow related optimizations such as strength reduction and invariant code motion (especially the latter) will have to be addressed in the future.

In principle, the **metastatement** abstraction is powerful enough to encapsulate language level parallel constructs. The author hopes to extend **META4** to handle parallel and distributed computation in the future.

While available literature is filled with separate research efforts regarding specialization ?, ?, optimization ?, ?, runtime code generation ?, ?, ?, ? and object-oriented programming, there are precious few that explore the intersection of these research areas. Despite the lack of a formal partial evaluation framework, the author hypothesizes that the combination of the approaches presented in this work (*e.g.*, the language features, the RTCG/PE facilities and the efficient optimization engine) is powerful enough for all *principled* uses of specialization and RTCG in an object-oriented development system. Showing evidence to such is left for future research.

## Notes

1. except for (R)CP, which do not require a prepass.
2. SSA does add additional bulk to the code size. However the bloat is a constant factor because the added code is a simple rewrite of assignments and control-flow joins. In practice, the expansion is a small constant.
3. Unfortunately, hash functions are very difficult to analyze *exactly*, but experience indicates that a good hash function *can* be considered a constant for most kinds of inputs of fixed size.
4. The renaming is necessary, as these “properties” in **META4** are not strictly equivalent to member functions in other languages such as Java or C++?.
5. Whenever that may be.
6. *i.e.*, quadratic or  $N \log(N)$ ? complexity, as opposed to linear
7. The inlining/unrolling algorithms are omitted for space reasons
8. As defined in 1., the “intermediate form” worked on by the optimizer is the implementation, which is an array of **statements** and **metastatements**.
9. Since a pair of numbers denote an instance, an **instance\_list** is also a list of numbers.
10. When an instance *i* is assigned a **StorageTag** *s*, this implies that *i* now has a *value* associated with it. This value does *not necessarily* imply a *physical address*. The name is somewhat misleading but its use is retained for historical reasons.
11. For those that are interested, a more detailed outline of the linearity of these algorithms are presented in ?.

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